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				Application Number	Not assigned yet
				Filing Date	On even date herewith
				First Named Inventor	N. KOBAYASHI, et al.
				Art Unit	Not assigned yet
				Examiner Name	Not assigned yet
				Attorney Docket Number	520.39458CV2
Sheet	1	of	1		

U.S. PATENT DOCUMENTS

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
VH		Kawahara et al., "20-Mb/s Erase/Record Flash Memory by Asymmetrical Operation" Symposium on VLSI Circuits Digest of Technical Papers. 1996. pp. 174-175	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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